

Title:

TCAD Simulations of 3D Effects in Nonvolatile Memory Structures

Abstract:

The evolution of nonvolatile memories (NVMs) toward higher levels of integration is mainly based on decreasing the cell size, optimizing the geometry of the layer system, and introducing new materials. To meet these new technology requirements, more sophisticated TCAD simulation tools based on realistic and accurate models are needed to take into account the increasing relevance of three-dimensional (3D) effects.

In the present work, we apply 3D TCAD process and device simulations to flash nonvolatile memories to predict the impact of geometry-related issues on cell performances.

In particular, we show the geometric impact of polysilicon reoxidation and deposition of the oxide-nitride-oxide layers and we compare the effectiveness of different technology adaptations.

Further, we quantify, by comprehensive 3D device simulations, the effects on the cell performances due to the polysilicon overetch, that is, penetration of the floating gate into the shallow trench isolation and the rounding effects.

Finally, we extract by full 3D simulation the coupling coefficients of the flash cells and we investigate the impact of the shape and the material of the spacer. The proposed methodology is assessed and compared with the methods currently used in the industry.

All simulations are performed by an innovative device simulation tool and a 3D process emulator that generates boundary-conforming grids allowing for very fine descriptions of the complex geometry.

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